



PIN NO	Symbol	Function
1	VSS/K	GND/LED BKL(-)
2	VDD	power supply for logic
3	VO	Contrast adjustment
4	RS	H/L Reagister select signal
5	R/W	H/L Read/write signal
6	E	H/L Enable signal
7	DB0	H/L Data bus line
8	DB1	
9	DB2	
10	DB3	
11	DB4	
12	DB5	
13	DB6	
14	DB7	Power supply for BKL(+)
15	A	

奈格科技有限公司 Nergal Technology Co.,ltd			
Customer Approval:	绘图:	绘图编号: NCM1202A	单位: mm
	审核:	客户编号:	日期:
	批准:	图纸版号:	页数: